

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Takayoshi WATANABE et al.

Serial No.:

09/462,796

Filed:

January 13, 2000

Title:

METHOD FOR PRODUCING A SEMICONDUCTOR DEVICE WITH

PYRAMIDAL BUMP ELECTRODES BONDED ONTO PAD **ELECTRODES ARRANGED ON A SEMICONDUCTOR CHIP**

September 13, 2005

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Respectfully submitted,

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CIB/sdb (703) 684-1120

TPW/AF PTO/SB/122 (09-04)

Approved for use through 07/31/2006. OMB 0651-0035

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This collection of information is required by 37 CFR 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.